Amendments to the Claims

The listing of claims below replaces all prior versions and listings of claims.

Listing of Claims

Claims 1-7 (Canceled)

Claim 8 (Currently Amended): A method of receiving a codeword that comprises one or more information bits, a first set of error detection check bits and a second set of error detection check bits, the method comprising:

using the first set of error detection check bits to make a first determination whether the one or more information bits are error-free;

using the second set of error detection check bits to make a second determination whether the one or more information bits and the first set of error detection check bits are error-free;

accepting the one or more information bits only if the first determination indicates that the one or more information bits are error-free and the second determination indicates that the one or more information bits and the first set of error detection check bits are error-free; and

~~The method of claim 7, further comprising:~~

determining whether a first level of error detection or a second level of error detection is to be used, ~~and~~

wherein using the second set of error detection check bits to make the second determination whether the one or more information bits and the first set of error detection check bits are error-free, and accepting the one or more information bits only if the first determination indicates that the one or more information bits are error-free and the second determination indicates that the one or more information bits and the first set of error detection check bits are error-free are performed only if the second level of error detection is to be used.

Claim 9 (Original): The method of claim 8, further comprising:

if the first level of error detection is to be used, then accepting the one or more information bits if the first determination indicates that the one or more information bits are error-free.

Claim 10 (Original): The method of claim 8, wherein determining whether the first level of error detection or the second level of error detection is to be used comprises:

determining whether the codeword is a retransmitted codeword.

Claim 11 (Original): The method of claim 8, wherein determining whether the first level of error detection or the second level of error detection is to be used comprises:

determining how many times the codeword was retransmitted.

Claim 12 (Original): The method of claim 8, wherein:

the codeword includes an indication of how many sets of error detection check bits there are in the codeword; and

determining whether the first level of error detection or the second level of error detection is to be used comprises using the indication in the codeword to determine whether the first level of error detection or the second level of error detection is to be used.

Claim 13 (Original): The method of claim 8, wherein:

the codeword was received in a packet;

the packet further includes a header;

the header includes an indication of how many sets of error detection check bits there are in the codeword; and

determining whether the first level of error detection or the second level of error detection is to be used comprises using the indication in the header to determine whether the first level of error detection or the second level of error detection is to be used.

Claim 14 (Currently Amended): A method of receiving a codeword that comprises one or more information bits, a first set of error detection check bits and a second set of error detection check bits, the method comprising:

using the first set of error detection check bits to make a first determination whether the one or more information bits are error-free;

using the second set of error detection check bits to make a second determination whether the one or more information bits and the first set of error detection check bits are error-free; and

accepting the one or more information bits only if the first determination indicates that the one or more information bits are error-free and the second determination indicates that the one or more information bits and the first set of error detection check bits are error-free,

~~The method of claim 7,~~ wherein:

the codeword was generated by means of an error correction code that was applied to the one or more information bits, the first set of error detection check bits and the second set of error detection check bits;

and the method further comprises:

decoding the codeword using the error correction code.

Claim 15 (Canceled):

Claim 16 (Original): A method of receiving a codeword that comprises one or more information bits and a plurality of sets of error detection check bits, wherein each of the sets of error detection check bits is associated with a corresponding portion of the codeword, the method comprising:

determining how many error detection checks should be performed;

until the determined number of error detection checks are performed, repeatedly using a different one of the sets of error detection check bits to determine whether the corresponding portion of the received codeword contains an error; and

disregarding all remaining unused different ones of the sets of error detection check bits.

Claim 17 (Original): The method of claim 16, wherein determining how many error detection checks should be performed comprises determining whether the received codeword is a retransmitted codeword.

Claim 18 (Original): The method of claim 16, wherein determining how many error detection checks should be performed comprises determining how many times the received codeword has been retransmitted.

Claim 19 (Original): The method of claim 16, wherein:

the codeword includes an indication of how many sets of error detection check bits there are in the codeword; and

determining how many error detection checks should be performed comprises using the indication in the codeword to determine how many error detection checks should be performed.

Claim 20 (Original): The method of claim 16, wherein:

the codeword was received in a packet;

the packet further includes a header;

the header includes an indication of how many sets of error detection check bits there are in the codeword; and

determining how many error detection checks should be performed comprises using the indication in the header to determine how many error detection checks should be performed.

Claim 21 -27 (Canceled)

Claim 28 (Currently Amended): An apparatus for receiving a codeword that comprises one or more information bits, a first set of error detection check bits and a second set of error detection check bits, the apparatus comprising:

logic that uses the first set of error detection check bits to make a first determination whether the one or more information bits are error-free;

logic that uses the second set of error detection check bits to make a second determination whether the one or more information bits and the first set of error detection check bits are error-free;

logic that accepts the one or more information bits only if the first determination indicates that the one or more information bits are error-free and the second determination indicates that the one or more information bits and the first set of error detection check bits are error-free; and

~~The apparatus of claim 27, further comprising:~~

logic that determines whether a first level of error detection or a second level of error detection is to be used, ~~and~~

wherein the logic that uses the second set of error detection check bits to make the second determination whether the one or more information bits and the first set of error detection check bits are error-free, and the logic that accepts the one or more information bits only if the first determination indicates that the one or more information bits are error-free and the second determination indicates that the one or more information bits and the first set of error detection check bits are error-free operate only if the second level of error detection is to be used.

Claim 29 (Original): The apparatus of claim 28, further comprising:

logic that responds to a determination that the first level of error detection is to be used by accepting the one or more information bits if the first determination indicates that the one or more information bits are error-free.

Claim 30 (Original): The apparatus of claim 28, wherein the logic that determines whether the first level of error detection or the second level of error detection is to be used comprises:

logic that determines whether the codeword is a retransmitted codeword.

Claim 31 (Original): The method of claim 28, wherein the logic that determines whether the first level of error detection or the second level of error detection is to be used comprises:

logic that determines how many times the codeword was retransmitted.

Claim 32 (Original): The apparatus of claim 28, wherein:

the codeword includes an indication of how many sets of error detection check bits there are in the codeword; and

the logic that determines whether the first level of error detection or the second level of error detection is to be used comprises logic that uses the indication in the codeword to determine whether the first level of error detection or the second level of error detection is to be used.

Claim 33 (Original): The apparatus of claim 28, wherein:

the codeword was received in a packet;

the packet further includes a header;

the header includes an indication of how many sets of error detection check bits there are in the codeword; and

the logic that determines whether the first level of error detection or the second level of error detection is to be used comprises logic that uses the indication in the header to determine whether the first level of error detection or the second level of error detection is to be used.

Claim 34 (Currently Amended): An apparatus for receiving a codeword that comprises one or more information bits, a first set of error detection check bits and a second set of error detection check bits, the apparatus comprising:

logic that uses the first set of error detection check bits to make a first determination whether the one or more information bits are error-free;

logic that uses the second set of error detection check bits to make a second determination whether the one or more information bits and the first set of error detection check bits are error-free;

logic that accepts the one or more information bits only if the first determination indicates that the one or more information bits are error-free and the second determination indicates that the one or more information bits and the first set of error detection check bits are error-free; and

~~The apparatus of claim 27,~~ wherein:

the codeword was generated by means of an error correction code that was applied to the one or more information bits, the first set of error detection check bits and the second set of error detection check bits;

and the apparatus further comprises:

logic that decodes the codeword using the error correction code.

Claim 35 (Canceled)

Claim 36 (Original): An apparatus for receiving a codeword that comprises one or more information bits and a plurality of sets of error detection check bits, wherein each of the sets of error detection check bits is associated with a corresponding portion of the codeword, the apparatus comprising:

logic that determines how many error detection checks should be performed; and

error detection logic that operates until the determined number of error detection checks are performed, by repeatedly using a different one of the sets of error detection check bits to determine whether the corresponding portion of the received codeword contains an error,

wherein the error detection logic disregards all remaining unused different ones of the sets of error detection check bits.

Claim 37 (Original): The apparatus of claim 36, wherein the logic that determines how many error detection checks should be performed comprises logic that determines whether the received codeword is a retransmitted codeword.

Claim 38 (Original): The apparatus of claim 36, wherein the logic that determines how many error detection checks should be performed comprises logic that determines how many times the received codeword has been retransmitted.

Claim 39 (Original): The apparatus of claim 36, wherein:

the codeword includes an indication of how many sets of error detection check bits there are in the codeword; and

the logic that determines how many error detection checks should be performed comprises logic that uses the indication in the codeword to determine how many error detection checks should be performed.

Claim 40 (Original): The apparatus of claim 36, wherein:

the codeword was received in a packet;

the packet further includes a header;

the header includes an indication of how many sets of error detection check bits there are in the codeword; and

the logic that determines how many error detection checks should be performed comprises logic that uses the indication in the header to determine how many error detection checks should be performed.